

### **REMARKS/ARGUMENTS**

The Examiner objects to the drawings under 37 C.F.R. 1.83(a) in that the drawings must show every feature of the invention specified in the claims. The Examiner states that the limitation of a third constant current source connected in series with the third diode between the first port and the gate terminal of the first MOS transistor must be shown or the features cancelled from the claims. Later in the Official Action, in the "Response to Arguments", the Examiner states that the objection to the drawings under 37 C.F.R., 183(a) is because they fail to show a third constant current source connected in series with the third diode and the first port and the gate terminal of the first MOS transistor called for in Claim 12 is not shown as Figure 9, because the claim depends upon Claim 11 which depends upon Claim 10 and requires that the constant current source be connected to a series with a resistor which is not shown in Figure 9.

This problem was created in response to correcting the claim in view of the Examiner's objections to the recitation of a "third" constant current source in Claim 12 whereas no first or second constant current sources were recited. In order to resolve the issue, Claim 12 is now made dependent upon Claim 10 and Claim 10 made dependent upon Claim 9 so that the three constant current sources are recited and the resistor, which is first recited in Claim 11, is not part of the circuit recited in Claim 12. This should overcome the Examiner's rejections, and no new drawings are required.

The Examiner rejects Claims 1-2 and 25 under 35 U.S. C. 102(e) as being anticipated by McDaniel, prior art of record. These claims have been cancelled without prejudice.

The Examiner has allowed Claims 3-14 and 16-24.

Accordingly, Applicants believe that the Application, as amended, is in condition for allowance, and such action is respectfully requested. .

Respectfully submitted,

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